

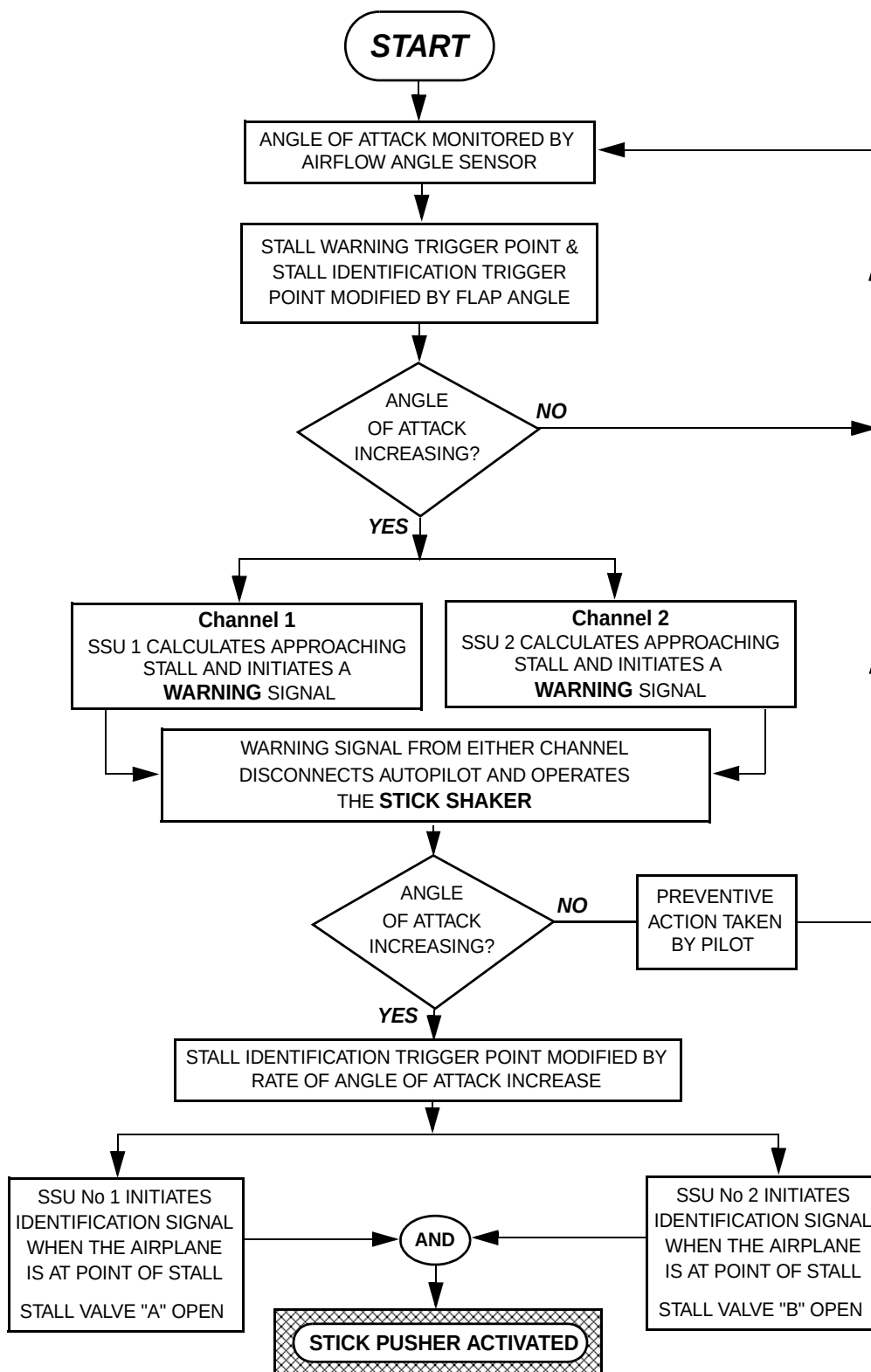


Figure 5
Stall System Flow Logic